



S/N 10/612793

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Arup Bhattacharyya	Examiner:	Fazli Erdem
Serial No.:	10/612793	Group Art Unit:	2826
Filed:	July 02, 2003	Docket:	1303.111US1
Title:	HIGH-PERFORMANCE ONE-TRANSISTOR MEMORY CELL		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

ARUP BHATTACHARYYA

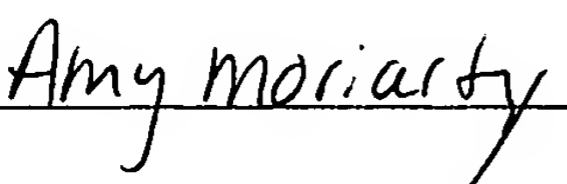
By his Representatives,


SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date 3-19-04

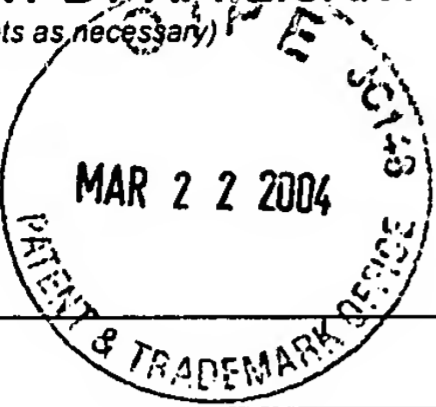
By   
Marvin L. Beekman  
Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19th day of March, 2004.

Name 

Signature 

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

<b>Substitute for form 1449A/PTO</b> <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i> 	<i>Complete if Known</i>	
	<b>Application Number</b>	10/612793
	<b>Filing Date</b>	July 2, 2003
	<b>First Named Inventor</b>	Bhattacharyya, Arup
	<b>Group Art Unit</b>	2826
	<b>Examiner Name</b>	Erdem, Fazli
Sheet 1 of 1	Attorney Docket No: 1303.111US1	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		FAZAN, P , et al., "Capacitor-Less 1-Transistor DRAM", <u>IEEE International SOI Conference</u> , (2002),10-13	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached